

**Abstract of the Disclosure**

~~The invention relates to a~~ A suppressor device for an electronic device comprising a plug-in device, comprising at least one plug element (~~8, 8'~~), which is arranged on a electrically conducting housing (~~1~~) of the electronic device. A printed circuit board (~~5~~) is arranged in the housing (~~1~~) and bears an electronic circuit leading to the plug element (~~8~~). A capacitor (~~19, 22~~) is connected to the plug element (~~8~~) and to the potential of the housing (~~1~~). The capacitor (~~19, 22~~) is arranged on the printed circuit board (~~5~~) which protrudes from the inner part of the housing (~~4~~) through an opening (~~3~~) with a part thereof (~~9~~) and which is also extends from the inner part of the housing (~~4~~) to the outer side of the housing (~~10~~). The plug element (~~8~~) is conductively connected to the capacitor (~~19, 22~~) and the circuit on the part (~~9~~) of the printed circuit board (~~5~~) located on the outer part of the housing (~~10~~).